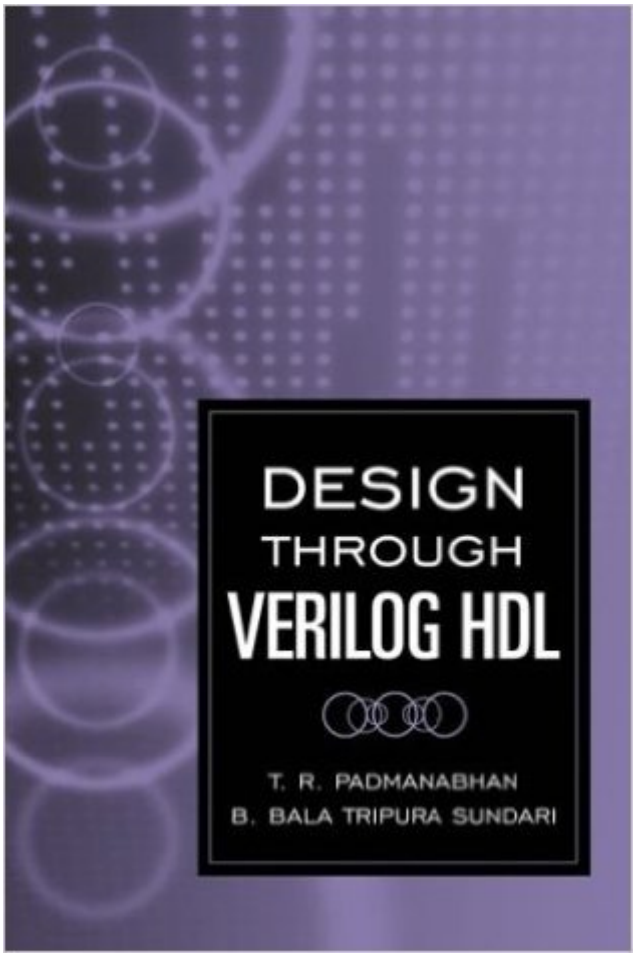
Design Through Verilog HDL



Synopsis

A comprehensive resource on Verilog HDL for beginners and experts Large and complicated digital circuits can be incorporated into hardware by using Verilog, a hardware description language (HDL). A designer aspiring to master this versatile language must first become familiar with its constructs, practice their use in real applications, and apply them in combinations in order to be successful. Design Through Verilog HDL affords novices the opportunity to perform all of these tasks, while also offering seasoned professionals a comprehensive resource on this dynamic tool. Describing a design using Verilog is only half the story: writing test-benches, testing a design for all its desired functions, and how identifying and removing the faults remain significant challenges. Design Through Verilog HDL addresses each of these issues concisely and effectively. The authors discuss constructs through illustrative examples that are tested with popular simulation packages, ensuring the subject matter remains practically relevant. Other important topics covered include: Primitives Gate and Net delays Buffers CMOS switches State machine design Further, the authors focus on illuminating the differences between gate level, data flow, and behavioral styles of Verilog, a critical distinction for designers. The book's final chapters deal with advanced topics such as timescales, parameters and related constructs, queues, and switch level design. Each chapter concludes with exercises that both ensure readers have mastered the present material and stimulate readers to explore avenues of their own choosing. Written and assembled in a paced, logical manner, Design Through Verilog HDL provides professionals, graduate students, and advanced undergraduates with a one-of-a-kind resource.

Book Information

Hardcover: 472 pages
Publisher: Wiley-IEEE Press; 1 edition (November 5, 2003)
Language: English
ISBN-10: 0471441481
ISBN-13: 978-0471441489
Product Dimensions: 6.4 x 1.1 x 9.5 inches
Shipping Weight: 1.7 pounds (View shipping rates and policies)
Average Customer Review: Be the first to review this item
Best Sellers Rank: #1,650,451 in Books (See Top 100 in Books) #68 inÂ Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > VLSI & ULSI #510 inÂ Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > Design #1184

Design Through Verilog HDL Digital Design: With an Introduction to the Verilog HDL 5th Ed. By Morris Mano (International Economy Edition) Verilog HDL Digital Design (Verilog): An Embedded Systems Approach Using Verilog The Verilog PLI Handbook: A User's Guide and Comprehensive Reference on the Verilog Programming Language Interface Learning FPGAs: Digital Design for Beginners with Mojo and Lucid HDL Digital Design with RTL Design, VHDL, and Verilog VLSI Chip Design with the Hardware Description Language VERILOG: An Introduction Based on a Large RISC Processor Design Ketogenic Recipes Box Set: 40 Low-Carb Breakfast Recipes To Reduce Your Weight plus Ketogenic Diet Plan to Improve the Ratio of HDL/LDL Cholesterol and ... Recipes books, Ketogenic Diet Books) Writing Testbenches: Functional Verification of HDL Models Digital VLSI Design with Verilog: A Textbook from Silicon Valley Polytechnic Institute Embedded SoPC Design with Nios II Processor and Verilog Examples Digital Integrated Circuit Design Using Verilog and Systemverilog Digital Systems Design: A Practical Approach: The Verilog Edition Digital VLSI Design with Verilog: A Textbook from Silicon Valley Technical Institute FPGA Prototyping By Verilog Examples: Xilinx Spartan-3 Version Programming FPGAs: Getting Started with Verilog Verilog Designer's Library Introduction to Verilog Feng Shui: Wellness and Peace- Interior Design, Home Decorating and Home Design (peace, home design, feng shui, home, design, home decor, prosperity)